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APPLICANTS

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** CONTINUING DATA *none*

** FOREIGN APPLICATIONS *DG*
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Verified and Acknowledged <i>[Signature]</i> Examining's Signature	JAPAN	4	12	7

ADDRESS
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TITLE
Semiconductor integrated circuit and method for testing same

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
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